

FIG. 1 is a block diagram of a network architecture. The diagram shows a central switch (14) connected to two other switches (12 and 16). Switch 12 is connected to a queue (44) and a shadow queue (56). Switch 16 is connected to a queue (62) and a shadow queue (64). The queues are connected to a set of output ports (20, 22, 24, 26, 28, 30). The shadow queues are connected to a set of output ports (32, 34, 36, 38, 40, 42). The diagram illustrates a network architecture with multiple switches and queues, and their connections to output ports.

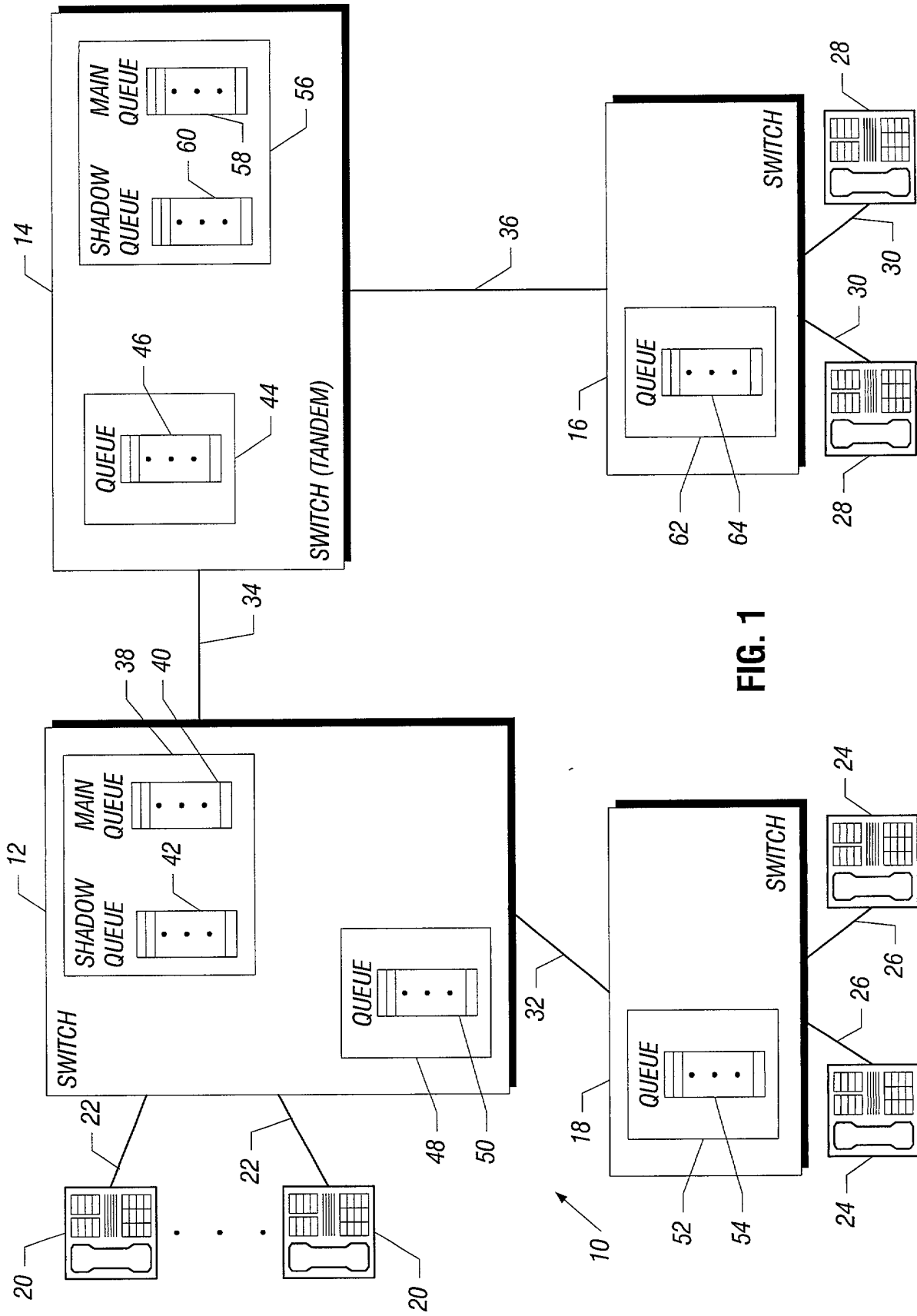


FIG. 1

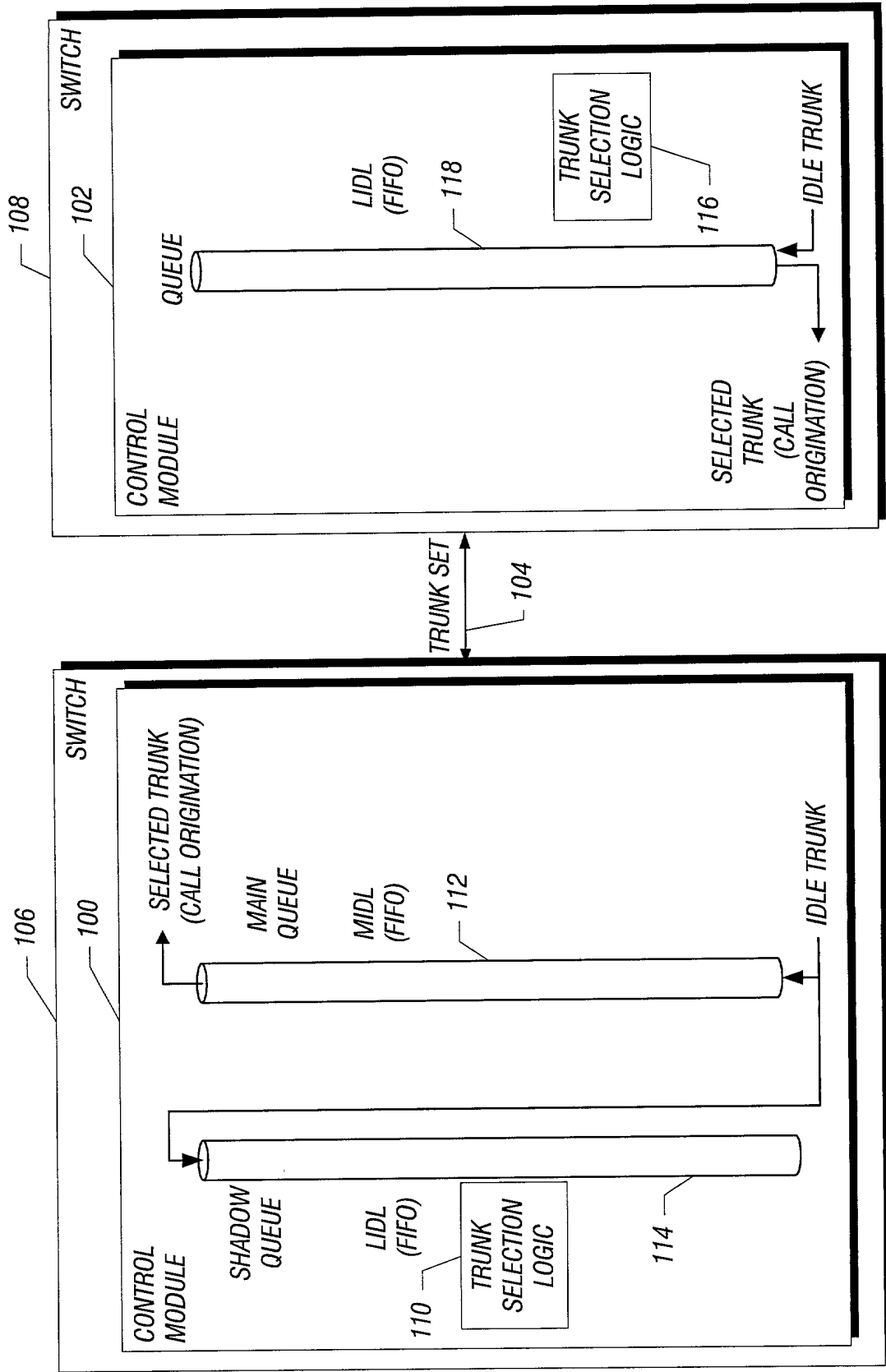


FIG. 2

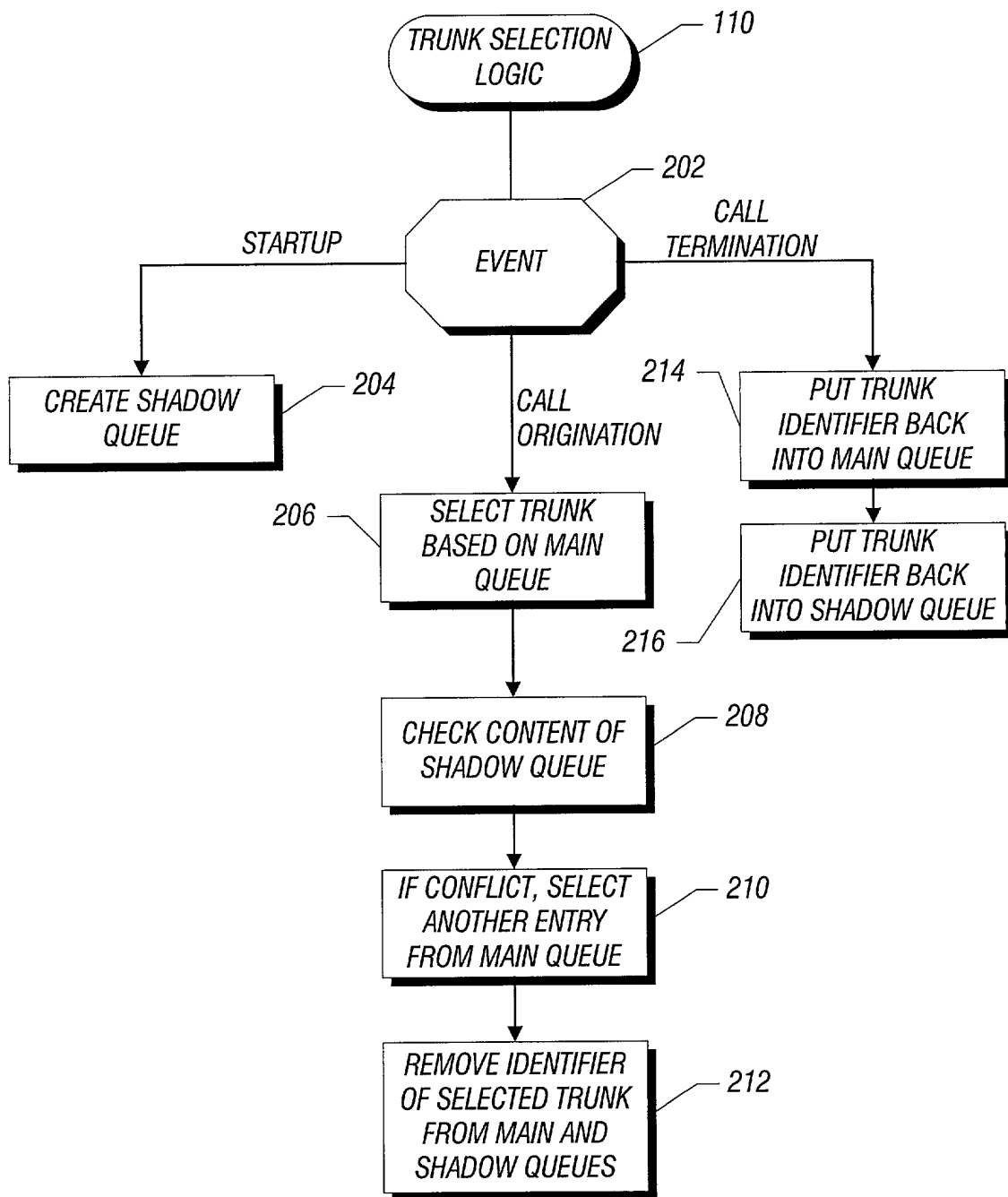
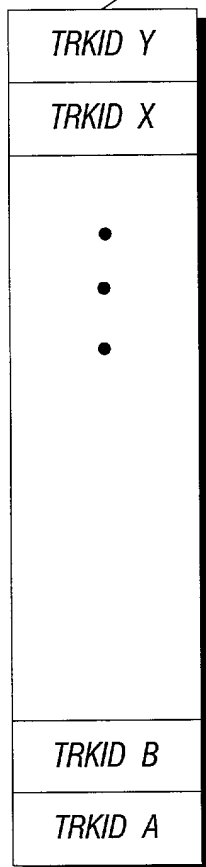


FIG. 3

SHADOW
QUEUE

114



MAIN
QUEUE

112

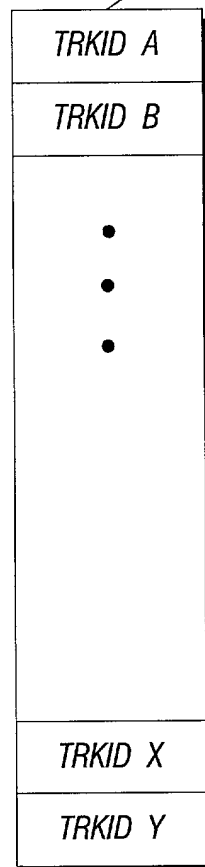


FIG. 4

FIG. 5 is a block diagram of a switch 300 in accordance with one embodiment of the present invention. The switch 300 includes a processing module 304, a messaging module 320, a switching bus 302, and a plurality of peripheral modules 306. The processing module 304 includes a processor 308, storage 310, trunk selection logic 312, trunk selection queues 314, and call control logic 316. The messaging module 320 is connected to the processing module 304 and a signaling interface. The switching bus 302 is connected to the processing module 304 and the peripheral modules 306. The peripheral modules 306 are connected to the switching bus 302 and a switch 300.

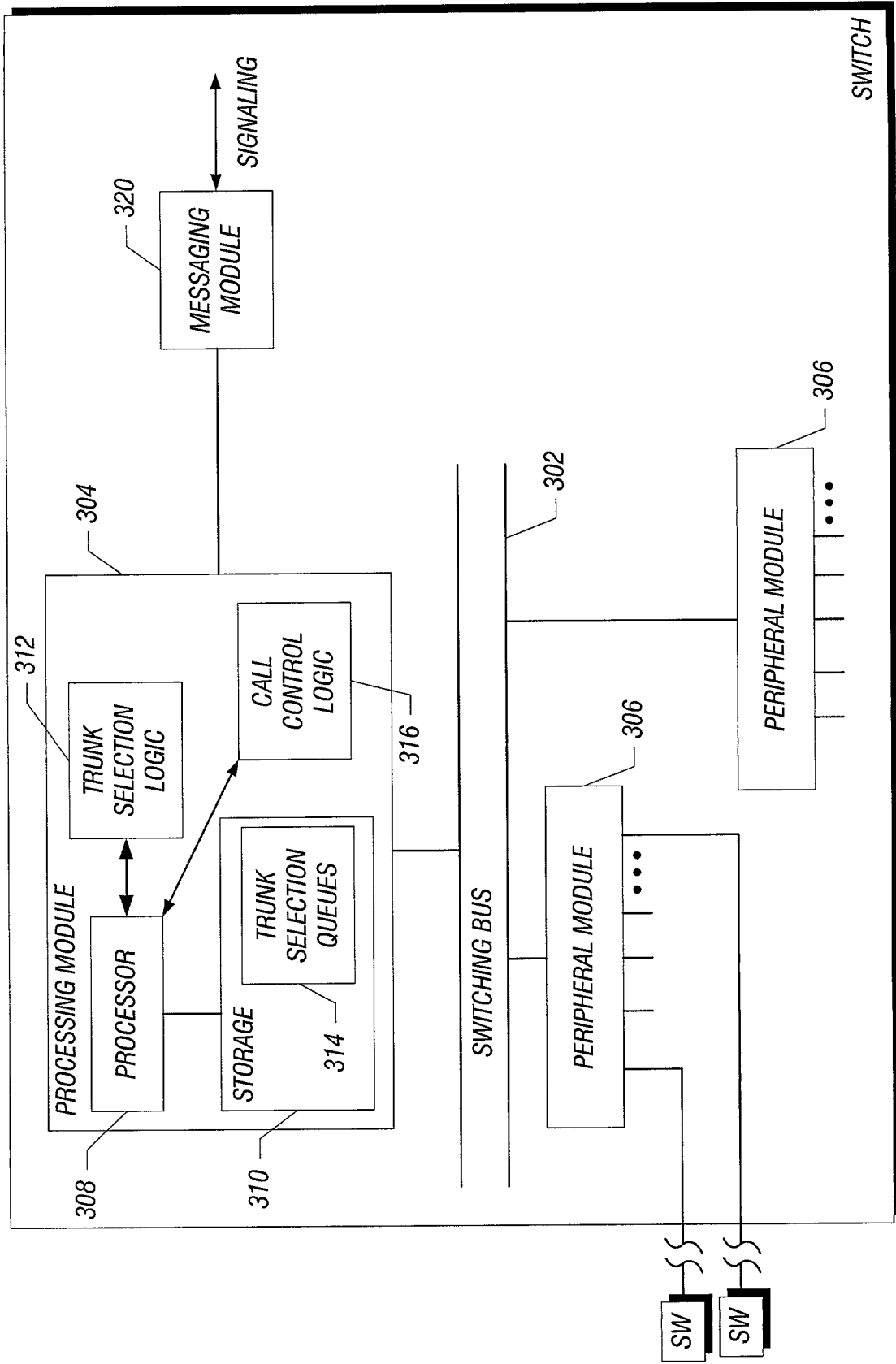


FIG. 5